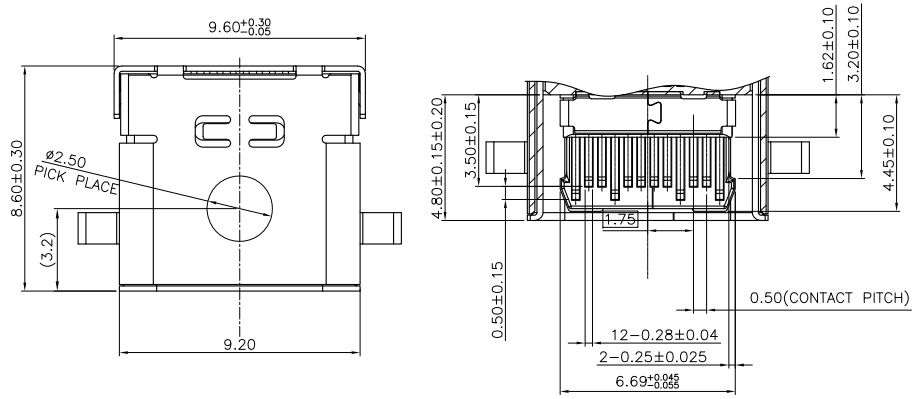
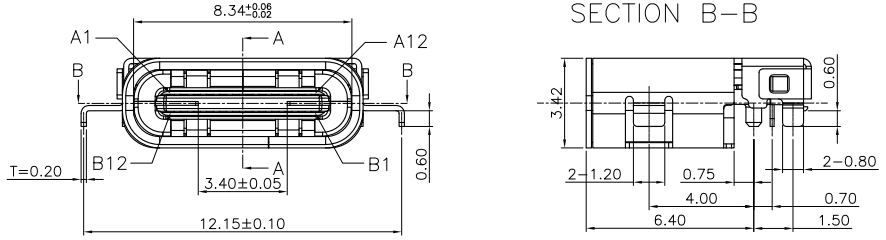
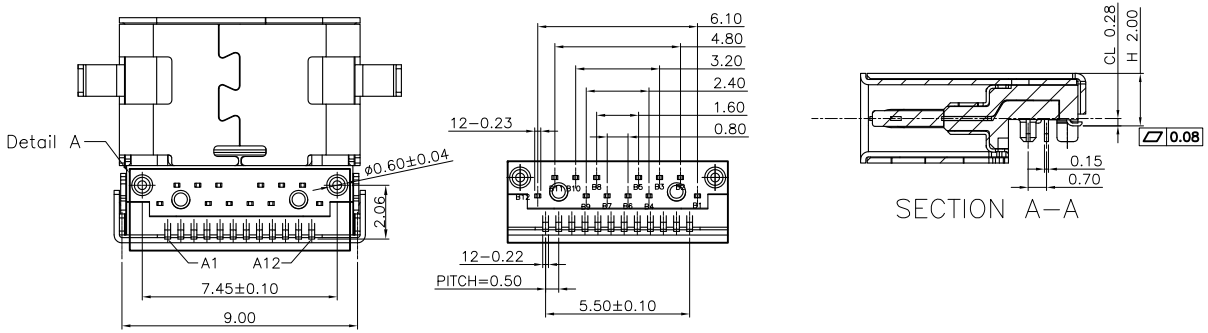
The product using material and processing must conform to the "WI-PZ-001 "HSF technical standard control requirements

RoHS



SECTION B-B





Detail A

SECTION A-A

NOTE:
1.MATERIAL SPECIFICATION:
 1-1.HOUSING:LCP BLACK UL94 V-0
 1-2.CONTACTS:COPPER ALLOY
 1-3.MID PLATE: STAINLESS STEEL SUS301
 1-4.SHELL: STAINLESS STEEL SUS304
2.PLATING SPECIFICATION:
 2-1.CONTACTS:
 Ni 50u" MIN. UNDER PLATED OVER ALL.
 Au PLATED ON THE FUNCTIONAL AREA OF CONTACT.
 2-2.MID PLATE:
 CLEAR ONLY
 2-3.FRONT SHELL:
 Ni 30u" MIN. UNDER PLATED OVER ALL.
3.MECHANICAL PERFORMANCE:
 3-1.INSERTION FORCE: 0.5~2.0kgf.
 3-2.REMOVAL FORCE: 0.8kgf~2.0kgf.
 3-3.DURABILITY: 10000 CYCLES.
4.ELECTRICAL PERFORMANCE:
 4-1. CURRENT RATING:5.0A
  VOLTAGE RATING:20V MAX
 4-2. LLCR:
  VBUS & GND PINS AND OTHER PINS: 40mΩ/PIN MAX.
  SHIELD: 50mΩ/MAX.
  LLCR MAX. CHANGE OF ALL PINS: 10mΩ.
 4-3.INSULATION RESISTANCE: 100MΩ MIN
 4-4.WITHSTANDING VOLTAGE: 100V AC R.M.S.
5.ENVIRONMENTAL PERFORMANCE:
 OPERATING TEMPERATURE: -25°C~+85°C.
6.PACKAGE SPEC:TAPE&REEL

WLT4F9-24 8 X X 0016

PLATING SPECIFICATION:
1-G/F
2-AU 3U"
5-AU15U"
6-AU30U"

Colour:
A:Black
K:White

REV.	REVISION RECORD	DATE
A0	NEW RELEASE	21.01.13

UNIT: mm
SIZE: A4

GENERAL TOLERANCES	
LINEAR	ANGLES
0.0±0.35	X° REF±6°
0.00±0.25	X° ±3°
0.000±0.10	X° X'±2°

SCALE: 1:1	NAME	DATE
APPROVED	Wang_jr	21.01.13
DESIGNER	Ding_bo	21.01.13
DRAWN	Ou_jian	21.01.13

PART.NO: WLT4F9-248XX0016

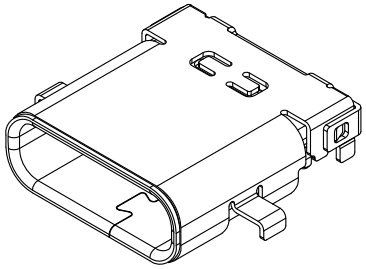
DWG.NO: ENDE05

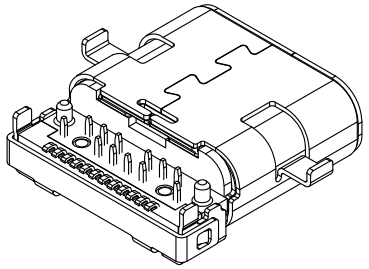
TITLE: TYPE C 3.1 24P/F SMT+DIP 沉板式

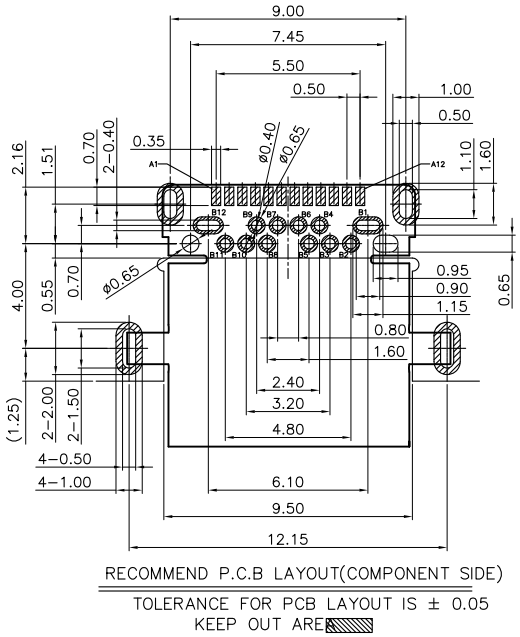
WanLian Teconology Co., Ltd

REV: A0

SHEET: 1/2







RECOMMEND P.C.B LAYOUT(COMPONENT SIDE)

TOLERANCE FOR PCB LAYOUT IS ± 0.05

KEEP OUT ARE

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	VBUS	Bus Power	B9	VBUS	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use(SBU)	B5	CC2	Configuraation Channel
A9	VBUS	Bus Power	B4	VBUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return

REV.	REVISION RECORD	DATE
A0	NEW RELEASE	21.01.13

UNIT: mm

SIZE: A4

GENERAL TOLERANCES	
LINEAR	ANGLES
0.0±0.35	X° REF±6°
0.00±0.25	X° ±3°
0.000±0.10	X° X'±2°

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APPROVED	Wang_jr	21.01.13
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DRAWN	Ou_jian	21.01.13

PART.NO: WLT4F9-248XX0016

DWG.NO: ENDE05

TITLE: TYPE C 3.1 24P/F SMT+DIP 沉板式

万连科技 WanLian Teconology Co., Ltd

REV: A0

SHEET: 2/2